## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re l	U.S. Patent Application of	. `	
НАТА	et al.	;	)))))))))
Applic	cation Number: To be Assigned	;	
Filed:	Concurrently Herewith	;	)
For:	A SEMICONDUCTOR DEVICE AND A METHOD OF MANUFACTURING THE SAME	;	,))
ΔΤΤΟΙ	PNEV DOCKET NO. HITA 0543	;	) \

Honorable Assistant Commissioner for Patents
Washington, D.C. 20231

## INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97, this Information Disclosure Statement is submitted in the above-identified patent application. A listing of documents to be published on the face of any patent granted from this application is submitted herewith on Form PTO-1449. Any other documents or information submitted for consideration by the Examiner are listed in this paper. A copy of each U.S. and foreign patent, or each publication or portion thereof listed or herein identified, is submitted herewith.

This Information Disclosure Statement is submitted with the initial filing of the application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

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April 21, 2004

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	U.S. Department of Commerce	APPLICANT						
	Patent and Trademark Office	HATA et al.						
		FILING DATE Concurrently Herewith		GROUP	GROUP			
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		IIS Patent	Documents		<del></del>			
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		Foreign P	atent Documents					
Examiner	DOCUMENT NUMBER	FILING DATE	Country	CLASS	SUB- CLASS	Translation		
Initial						YES	No	
	2000-223634	1/28/99	Japan			Abstract	X	
	2003-86787	9/13/2001	Japan			Abstract	X	
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